



**OPEN**  
Compute Project

**Alpha Networks Inc**

**SNQ-60x0-320F**

32-port 40G QSFP Switch  
(ToR/Aggregation Switch)

Author: Damon Lee, Chloe Lin

## Revision History

Version	Revised Date	Author	Content Revised
<b>0.1</b>	12/02/13	Damon Lee	First Drafted
<b>0.2</b>	12/13/13	Damon Lee	1. Updated the LED definition for MGMT and Console port
<b>0.3</b>	12/20/13	Damon Lee	1. Updated the LED definition for 40G and 10G mode 2. Updated the FAN module 3. Updated the schedule 4. Updated PSU detail
<b>0.4</b>	03/21/14	Damon Lee	1. Added Micro USB console port 2. Updated the LED definition 3. Updated front and rear panel
<b>0.5</b>	05/28/14	Damon Lee	1. Updated the LED definition 2. Updated Power supply module 3. Updated the FAN module
<b>0.6</b>	09/24/14	Chloe Lin	Add Fan module connector part number

# Scope

This documents defines the technical specification for SNQ-60x0-320F used in the Open Compute Project as 40G Top of the Rack (ToR) or as an aggregation switch

# Contents

Revision History.....	2
Overview .....	6
License.....	6
1 Feature Highlights.....	8
2 Physical Overview .....	9
2.1 Mechanical Dimension.....	9
2.2 Top View.....	10
2.3 Front View.....	11
2.4 Rear View .....	11
3 LED Definition .....	12
4 Field Replaceable Components .....	14
4.1 Power Supply Modules .....	14
4.2 Fan Modules.....	15
5 System Overview .....	16
5.1 Main PCB.....	16
5.1 CPU Subsystem.....	18
5.1.1 Freescale CPU (P2020).....	19
5.1.1.1 DDR3 SDRAM .....	19
5.1.1.2 PCIe Interface.....	19
5.1.2 Intel CPU (C2558).....	20

5.1.2.1	DDR3 SDRAM .....	20
5.1.2.2	PCIe Interface .....	20
6	IO and Connectors .....	21
6.1	RS232 Interface .....	21
6.2	Management Ethernet Interfaces.....	21
6.3	USB Interface .....	21
7	Power/Environmental/Agency Certifications .....	21

## List of Figures

Figure 1: SNQ-60x0-320F Chassis dimension.....	9
Figure 2: SNQ-60x0-320F top view .....	10
Figure 3: SNQ-60x0-320F front view.....	11
Figure 4: SNQ-60x0-320F rear view .....	11
Figure 5: Power Supply Mechanical specification.....	15
Figure 6: Fan module mechanical specification.....	16
Figure 7: Main board block diagram .....	17
Figure 8: Main PCB top view .....	18
Figure 9: Freescale CPU board block diagram.....	19
Figure 10: Intel CPU board block diagram .....	20

## List of Tables

Table 1: LED behavior for Port 1~32 40G Ethernet Port .....	12
Table 2: LED behavior for Port 1~32 40G Ethernet Port .....	13
Table 3: LED behavior for Port 1~104 10G Ethernet Port .....	13



Open Compute Project Alpha Networks SNQ-60x0-320F Specification v0.5

Table 4: Power supply LED definition.....	13
Table 5: Power supplies details .....	14
Table 6: Power supply connector pint out .....	14
Table 7: Fan Modules part number .....	15
Table 8: Fan Modules connector pin out .....	15
Table 9: PCBs for SNQ-60x0-320F .....	16
Table 10: CPU subsystem key Components .....	19
Table 11: Power consumption and environment table.....	22
Table 12: Regulatory Standards Compliance table .....	23

## Overview

The SNQ-60x0-320F Series Data Center, Top-of-Rack (ToR)/aggregation switches, with a total combined bandwidth of 1,280 Gbps, feature 32 ports of 40 Gbps Ethernet wire-speeds or up to 104 ports of 10 Gbps Ethernet wire-speeds. The Layer 3 capable, bare metal system also provides an RJ-45 and a Micro-USB console port and an Out-Of-Band (OOB) management port. The SNQ-60x0-320F switch is a PHY-less design with QSFP+ connections directly attached to the SERDES interface of Broadcom BCM56850.

## License

All semiconductor devices that may be referred to in this specification, or required to manufacture products described in this specification, will be considered referenced only, and no intellectual property rights embodied in or covering such semiconductor devices shall be licensed as a result of this specification or such references. Notwithstanding anything to the contrary in the CLA, the licenses set forth therein do not apply to the intellectual property rights included in or related to the semi-conductor devices identifies in the specification. These references include without limitation the reference to devices listed below. For clarity, no patent claim that reads on such semiconductor devices will be considered a “Granted Claim” under the applicable Contributor License Agreement for this specification.

Manufacturer	Description
<b>Broadcom</b>	BCM56854
<b>Intel</b>	x86 CPU C2538-2.4GHz
<b>Freescale</b>	P2020NSN2MHC
<b>Marvell</b>	88E1112
<b>Transcend</b>	SODIMM TS512MSK72V3N
<b>Transcend</b>	SD Card TS8GUSDC10M
<b>Macronix</b>	Flash MX29LV640EBTI-70G
<b>Renesas</b>	EEPROM R1EX24002ASAS0I
<b>Atmel</b>	AT24C128C-SSHM-T
<b>Lattice</b>	LCMXO256C-3TN100C



As of July 11, 2014, the following persons or entities have made this Specification available under the Open Web Foundation Final Specification Agreement (OWFa 1.0), which is available at <http://www.openwebfoundation.org/legal/the-owf-1-0-agreements/owfa-1-0>:

Alpha Networks Inc.

You can review the signed copies of the Open Web Foundation Agreement Version 1.0 for this Specification at <http://opencompute.org/licensing/>, which may also include additional parties to those listed above.

Your use of this Specification may be subject to other third party rights. THIS SPECIFICATION IS PROVIDED "AS IS." No support of any kind will be provided by the contributors. The contributors expressly disclaim any warranties (express, implied, or otherwise), including implied warranties of merchantability, non-infringement, fitness for a particular purpose, or title, related to the Specification. The entire risk as to implementing or otherwise using the Specification is assumed by the Specification implementer and user. IN NO EVENT WILL ANY PARTY BE LIABLE TO ANY OTHER PARTY FOR LOST PROFITS OR ANY FORM OF INDIRECT, SPECIAL, INCIDENTAL, OR CONSEQUENTIAL DAMAGES OF ANY CHARACTER FROM ANY CAUSES OF ACTION OF ANY KIND WITH RESPECT TO THIS SPECIFICATION OR ITS GOVERNING AGREEMENT, WHETHER BASED ON BREACH OF CONTRACT, TORT (INCLUDING NEGLIGENCE), OR OTHERWISE, AND WHETHER OR NOT THE OTHER PARTY HAS BEEN ADVISED OF THE POSSIBILITY OF SUCH DAMAGE.

# 1 Feature Highlights

The SNQ-60x0-320F Series Data Center, Top-of-Rack (ToR) switches is a cost optimized switch, with a total combined bandwidth of 1,280 Gbps, feature 32 ports of 40 Gbps Ethernet wire-speeds or up to 104 ports of 10 Gbps Ethernet wire-speeds. The Layer 3 capable, bare metal system also provides an RJ-45 and a Micro-USB console port and an Out-Of-Band (OOB) management port. Administrators can selectively access the Command Line Interface (CLI) through either the RJ-45 console port or the Micro-USB console port by simple toggling the dip switch on the front panel of the switch.

- Modular CPU board with large flash and memory
- Temperature warning
- Software-readable thermal monitor
- Real time clock (RTC) support
- Two Hot-swappable redundant power supply
- Five redundant (4+1) fan modules
- One 10/100/1000 Mbps management port
- One RJ45 type console port in the front panel
- One Micro USB console port
- One USB port in the front panel for hosting an external USB flash
- One Reset button in the front panel



## 2 Physical Overview

### 2.1 Mechanical Dimension

Dimension	
Height x Width x Depth	44mm(H)440mm(W) x 487.4 mm(D)

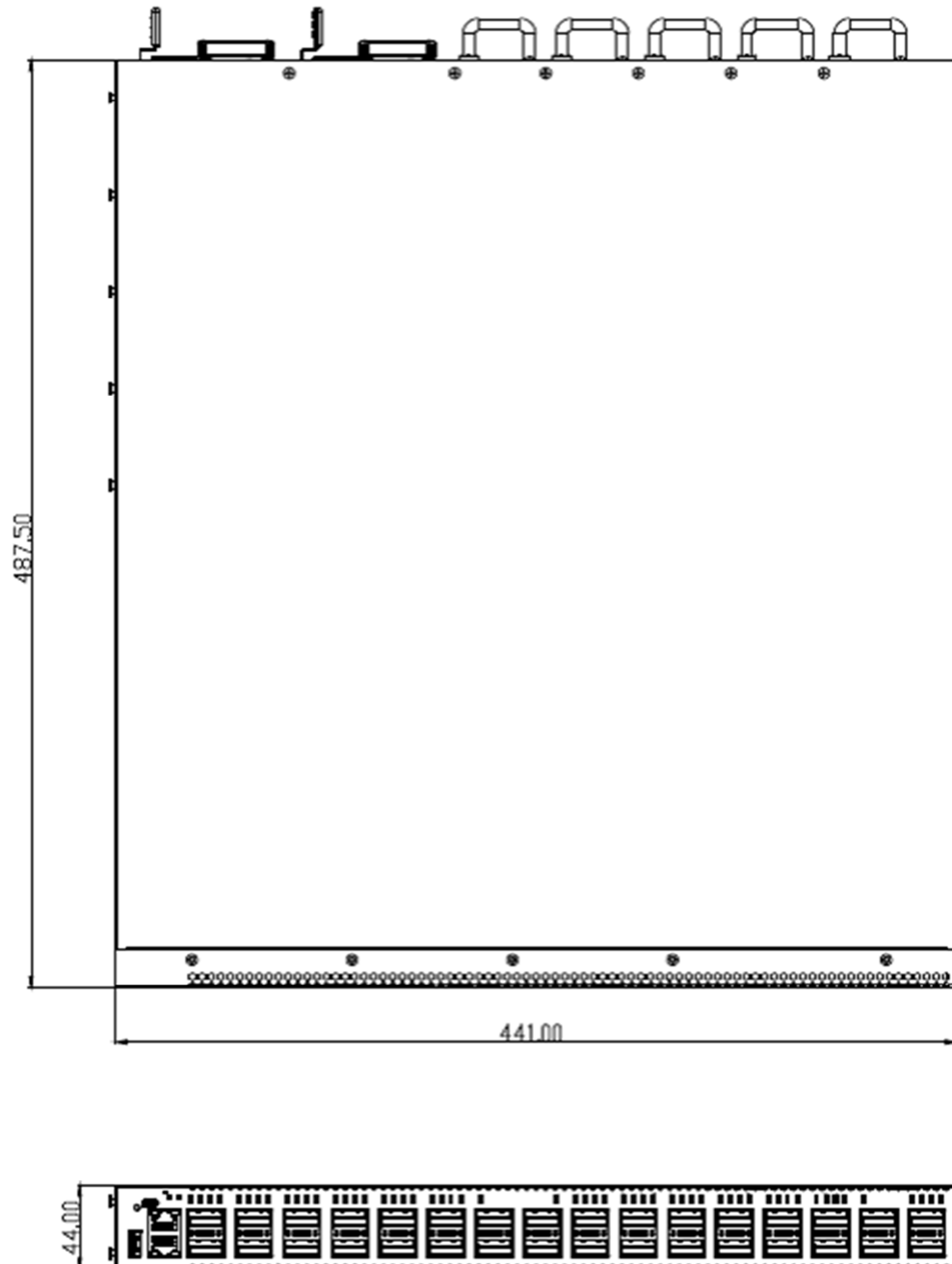
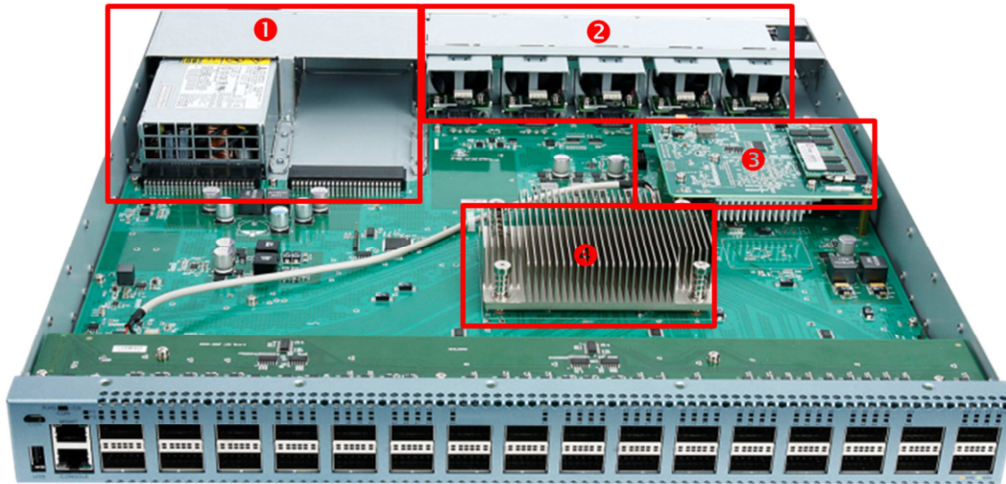


Figure 1: SNQ-60x0-320F Chassis dimension

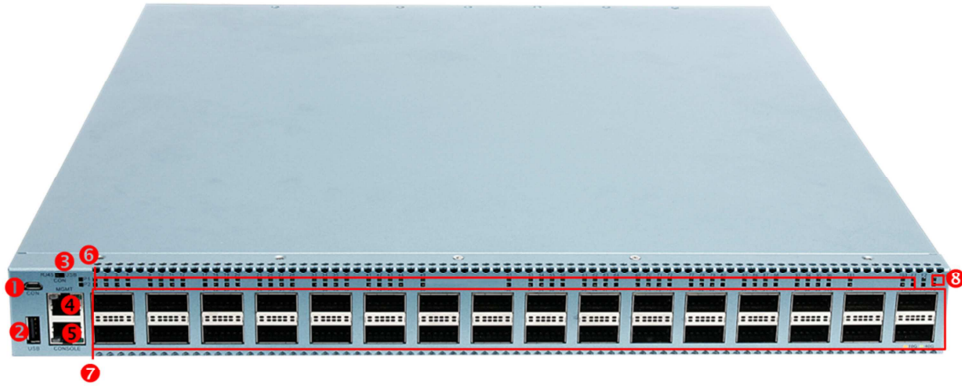
## 2.2 Top View



- ❶: Hot swappable power supply
- ❷: Hot swappable fan modules
- ❸: CPU module
- ❹: Switch MAC – Trident 2

Figure 2: SNQ-60x0-320F top view

## 2.3 Front View



- ❶: Mini USB console port
- ❷: Type A USB storage port
- ❸: Console port selection switch
- ❹: Out of band management port
- ❺: Console port
- ❻: QSFPP ports LED
- ❼: 40G QSFP ports
- ❼: Blue Locator LED

Figure 3: SNQ-60x0-320F front view

## 2.4 Rear View



- ❶: Hot swappable fan modules
- ❷: Hot swappable power supply

Figure 4: SNQ-60x0-320F rear view

### 3 LED Definition

The following table defines the per device LEDs' behaviors:

LED Indication	Color	Behavior	Description
PWR	Green	Solid Light	Power On
		Off	Power Off and no power attached
	Amber	Blinking	Power supply failures, over voltage, over current, over temperature
SYST	Green	Solid Light	POST Passed, normal operation
		Blinking	POST in progress
		Light off	No power
	Amber	Blinking	POST failed or overheat or power supply failed or Fan module fail or over temperature
Locator	Blue	Blinking	Locator function is enable
		Off	Locator function is disable
FAN 1 FAN 2 FAN 3 FAN 4 FAN 5	Green	Solid Light	All diagnostics pass. The module is operational.
		Off	The module is not receiving power
	Amber	Blinking	Failure
MGMT	Green(R)	Solid Light	Link up
		Blinking	Packet transmitting or receiving
		Light off	No link up or port disable
CON	Green(R)	Solid Light	Console on
		Light off	Console off

Table 1: LED behavior for Port 1~32 40G Ethernet Port

The following defines the 40G QSFP+ Ethernet port LEDs' behaviors:

Location	LED Indication	Color	Behavior	Description
<b>LED number</b> <b>1~48,</b> <b>53~100</b>	Link/Act/Speed	Green	Solid Light	When there is a secure 40G connection (or link)
			Blinking	Packet transmitting or

<b>group of 4 (40Gbps)</b>				receiving
			Light off	No link up or port disable
<b>LED number 49~52, 101~104 (40Gbps)</b>	Link/Act/Speed	Green	Solid Light	When there is a secure 40G connection (or link)
			Blinking	Packet transmitting or receiving
			Light off	No link up or port disable

Table 2: LED behavior for Port 1~32 40G Ethernet Port

In the case of split cable plugged in to port 1 ~ 12, and 17 ~ 28 each ports will behaves as 4 individual 10G ports. The following table defines the 10G Ethernet port LEDs' behaviors when split cables are used:

Location	LED Indication	Color	Behavior	Description
<b>LED number 1~48, 53~100 (10Gbps)</b>	Link/Act/Speed	Amber	Solid Light	When there is a secure 10G connection (or link)
			Blinking	Packet transmitting or receiving
			Light off	No link up or port disable

Table 3: LED behavior for Port 1~104 10G Ethernet Port

Each power supply module has a bi-color LED, which behavior is descript in the following:

LED Color	Behavior	Description
<b>Green</b>	Solid Light	Output ON and OK
	Blinking	AC present / AC Line 12VSB Holdup
	Light off	No AC power to all power supplies
<b>Amber</b>	Solid Light	Power supply critical event causing a shutdown; failure, Fan Fail
	Blinking	Power supply warning events where the power supply continues to operate; high temp, high power, high current, slow fan.

Table 4: Power supply LED definition

## 4 Field Replaceable Components

### 4.1 Power Supply Modules

Then SNQ-60x0-320F supports two hot swappable power supplies plugged in at the same time for redundancy. The details of the power supplies are as following:

Power Supply	
<b>Number of power supply</b>	2
<b>Power supply types</b>	AC version (forward and reversed airflow) <ul style="list-style-type: none"> <li>● DPS-460KB C</li> <li>● DPS-460KB B</li> </ul> DC version (forward and reversed airflow) <ul style="list-style-type: none"> <li>● DPS-800KB C</li> <li>● DPS-800KB B</li> </ul>
<b>AC PSUs</b> <ul style="list-style-type: none"> <li>● <b>Input voltage</b></li> <li>● <b>Frequency</b></li> <li>● <b>Efficiency</b></li> </ul>	<ul style="list-style-type: none"> <li>● 100 to 240 VAC</li> <li>● 50 to 60 Hz</li> <li>● 89 to 91% at 220V</li> </ul>
<b>DC PSUs</b> <ul style="list-style-type: none"> <li>● <b>Input voltage range</b></li> <li>● <b>Efficiency</b></li> </ul>	<ul style="list-style-type: none"> <li>● 40.5V/23.8A 48V/19.0A -60V/15.6</li> <li>● 85 to 88%</li> </ul>

Table 5: Power supplies details

Pin #	Descriptin	Pin #	Descriptin3
<b>A1~9</b>	GND	<b>B1~9</b>	GND
<b>A10~18</b>	+12V	<b>B10~18</b>	+12V
<b>A19</b>	PMBus SDA	<b>B19</b>	A0 (SMBus Address)
<b>A20</b>	PMBus SCL	<b>B20</b>	N/A
<b>A21</b>	PSON	<b>B21</b>	12VSB
<b>A22</b>	SMBAlert#	<b>B22</b>	Smart_on
<b>A23</b>	Return Sense	<b>B23</b>	12VLS
<b>A24</b>	+12V Remote Sense	<b>B24</b>	No Connect
<b>A25</b>	PWOK	<b>B25</b>	N/A

Table 6: Power supply connector pint out

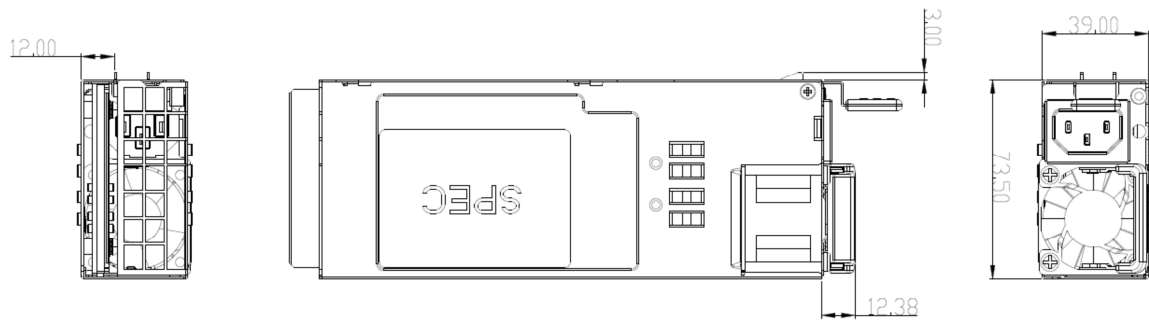


Figure 5: Power Supply Mechanical specification

## 4.2 Fan Modules

The SNQ-60x0-320F supports up to 5 fan modules. For front to rear and rear to front air flow, different types of fan modules are required.

Air Flow Direction	Part Number
Front to Rear	AVC DFTA0456B2UP057
Rear to Front	AVC DFTA0456B2UP058

Table 7: Fan Modules part number

Fan module connector: LCU SM401V-20P

#	NAME	Description	#	NAME	Description
1	FAN_CON_TACH_0	FAN tachometer 0	11	FAN_DIR	FAN Direction
2	GND	GND	12	GND	GND
3	FAN_12VIN	12V	13	FAN_12VIN	12V
4	FAN_CON_PWM_0	PWM control for FAN0	14	EE_GND	EEPROM GND
5			15	EE_SDA	EEPROM SDA
6	EE_SCL	EEPROM SCL	16	EE_VDD	EEPROM VDD
7	EE_A0	EEPROM ADDR_0	17	FAN_CON_PWM_1	PWM control for FAN1
8	FAN_12VIN	12V	18	FAN_12VIN	12V
9	GND	GND	19	GND	GND
10	FAN_PRESENT#	Exist FAN module	20	FAN_CON_TACH_1	FAN tachometer 0

Table 8: Fan Modules connector pin out

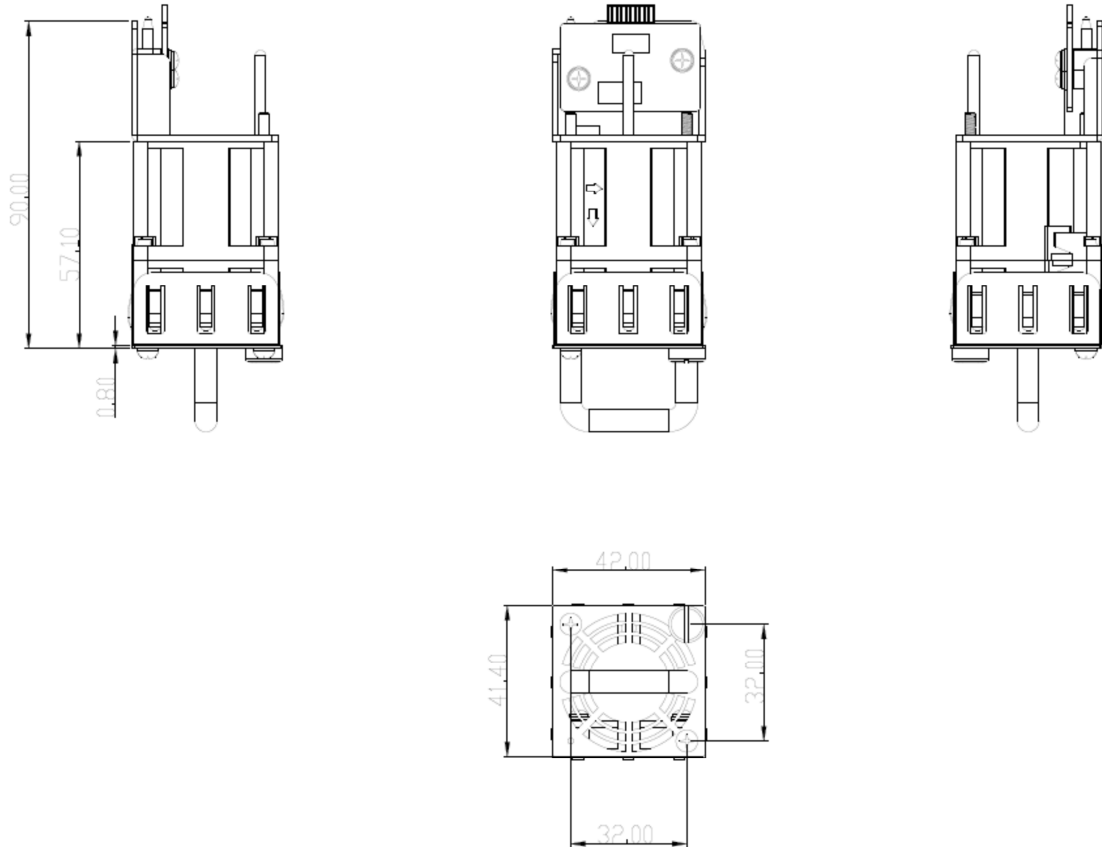


Figure 6: Fan module mechanical specification

## 5 System Overview

The SNQ-60x0-320F comprised of the following PCB

PCB Function	PCB Layer	Dimension (mmxmm)
Main board	12	432.5*390.6
FAN module	2	38.5*29
LED board	2	431*62
Freescale CPU board	6	120*109*1.6
Intel CPU board	12	255*165.1

Table 9: PCBs for SNQ-60x0-320F

### 5.1 Main PCB

The main PCB is a 12 layer PCB where the switch MAC resides. It also supports the following functions:

- Networking I/O ports



- Management ports
- LED
- Connectivity to power supply and fan
- Power conversion circuit
- etc's

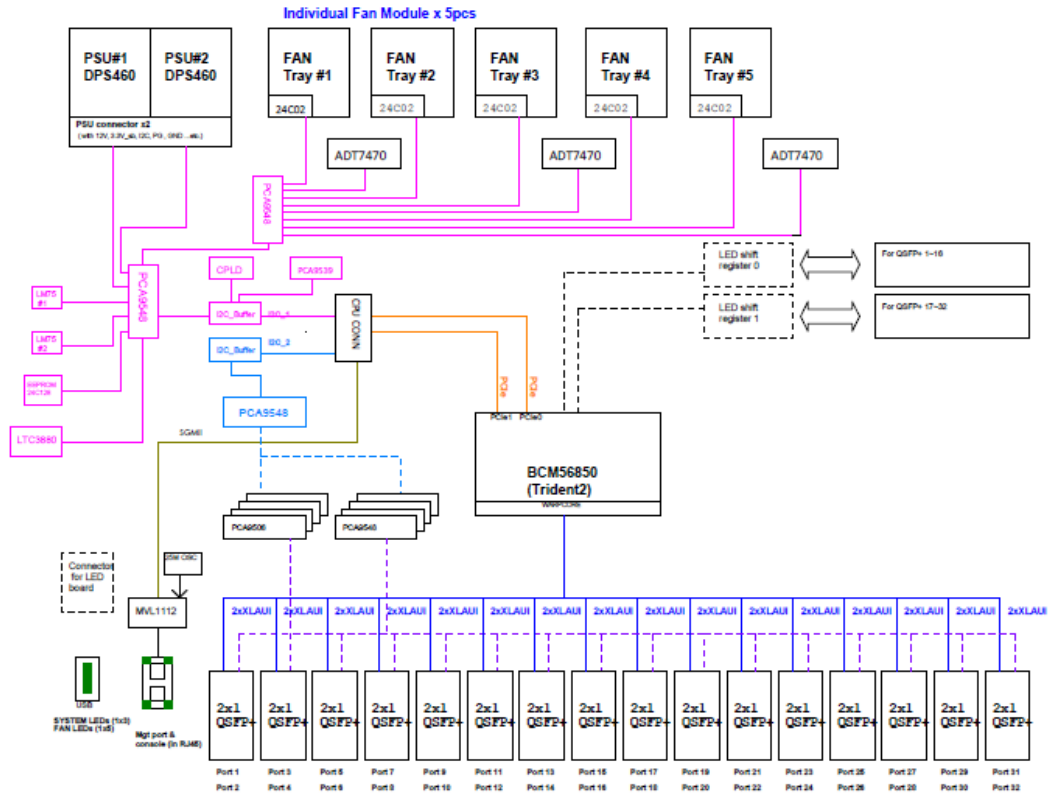


Figure 7: Main board block diagram

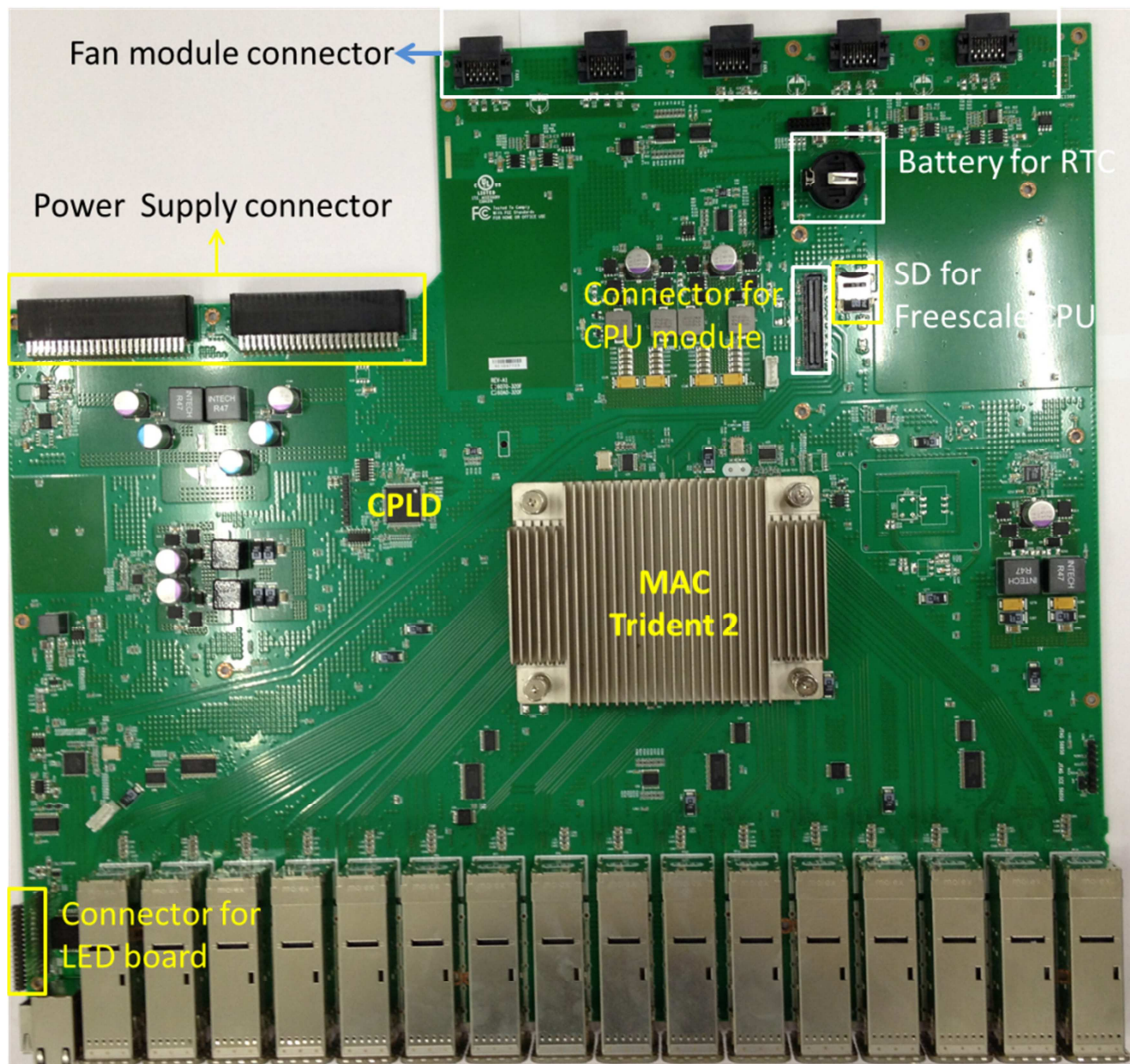


Figure 8: Main PCB top view

## 5.1 CPU Subsystem

The SNQ-60x0-320F offers CPU in modular form to allow the flexibility for different CPU preference. Currently two types of CPU modules are supported, and the detail is provided in the following table and sections.

Items		Detailed Description
Freescale		
<b>Modular CPU board (Option 1)</b>	CPU	Freescale P2020, 1.2GHz with PCIe connector to main board
	RAM	DDR3 4GB for Freescale CPU
	Flash	Micro-SD Card 8GB for Freescale CPU
	Boot Flash	8MB for Freescale CPU

Intel		
<b>Modular CPU board (Option 2)</b>	CPU	Intel Rangeley C2558 4 Cores/2.4G
	RAM	DDR3 4GB for Intel Rangeley CPU
	Flash	SSD 8GB for Intel Rangeley CPU
	Boot Flash	8MB for Intel Rangeley CPU

Table 10: CPU subsystem key Components

### 5.1.1 Freescale CPU (P2020)

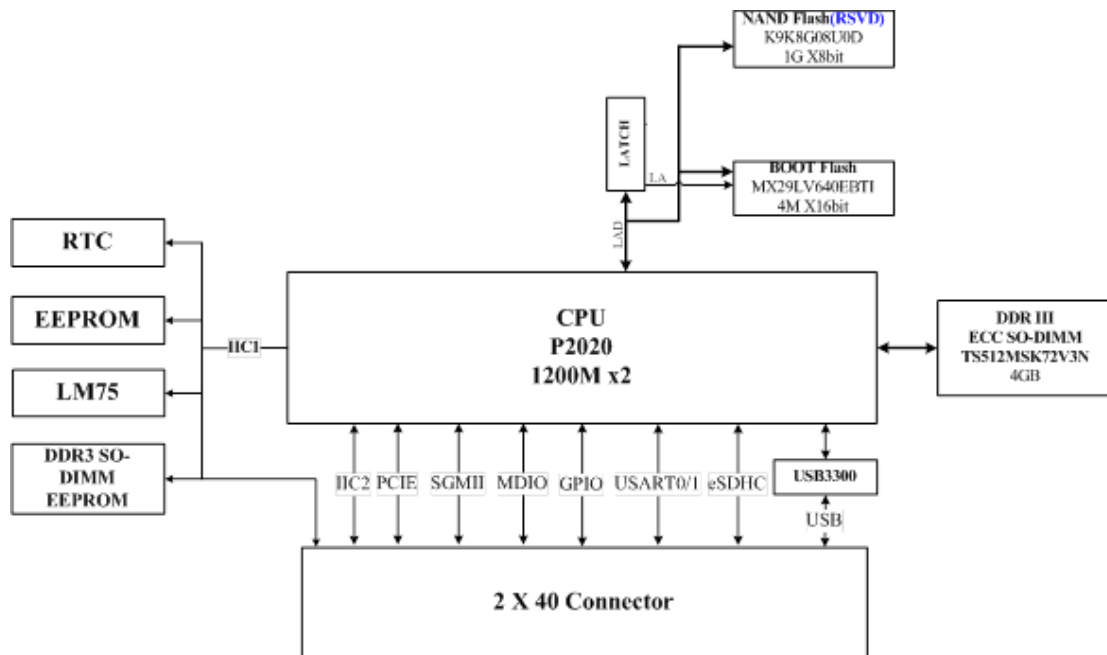


Figure 9: Freescale CPU board block diagram

#### 5.1.1.1 DDR3 SDRAM

The Freescale DDR SDRAM controller supports most JEDEC standard  $\times 8$ ,  $\times 16$ ,  $\times 32$ , or  $\times 64$  DDR2 and DDR3 memories available. Built-in error checking and correction (ECC) ensures very low bit-error rates for reliable high-frequency operation. Dynamic power management and auto-precharge modes simplify memory system design. The DDR memory controller includes these distinctive features:

- Support for DDR2 and DDR3 SDRAM
- 64-/72-bit SDRAM data bus, 32-/40-bit SDRAM for DDR2 and DDR3
- Support for up to 32Gbits of memory

#### 5.1.1.2 PCIe Interface

The P2020 supports three PCI Express interfaces that are compliant with the PCI

Express Base Specification Revision 1.0a. The physical layer of the PCI Express interface operates at a transmission rate of 2.5 Gbaud (data rate of 2.0 Gbps) per lane. The theoretical unidirectional peak bandwidth is 2 Gbps per lane. Receive and transmit ports operate independently, resulting in an aggregate theoretical bandwidth of 4 Gbps per lane.

### 5.1.2 Intel CPU (C2558)

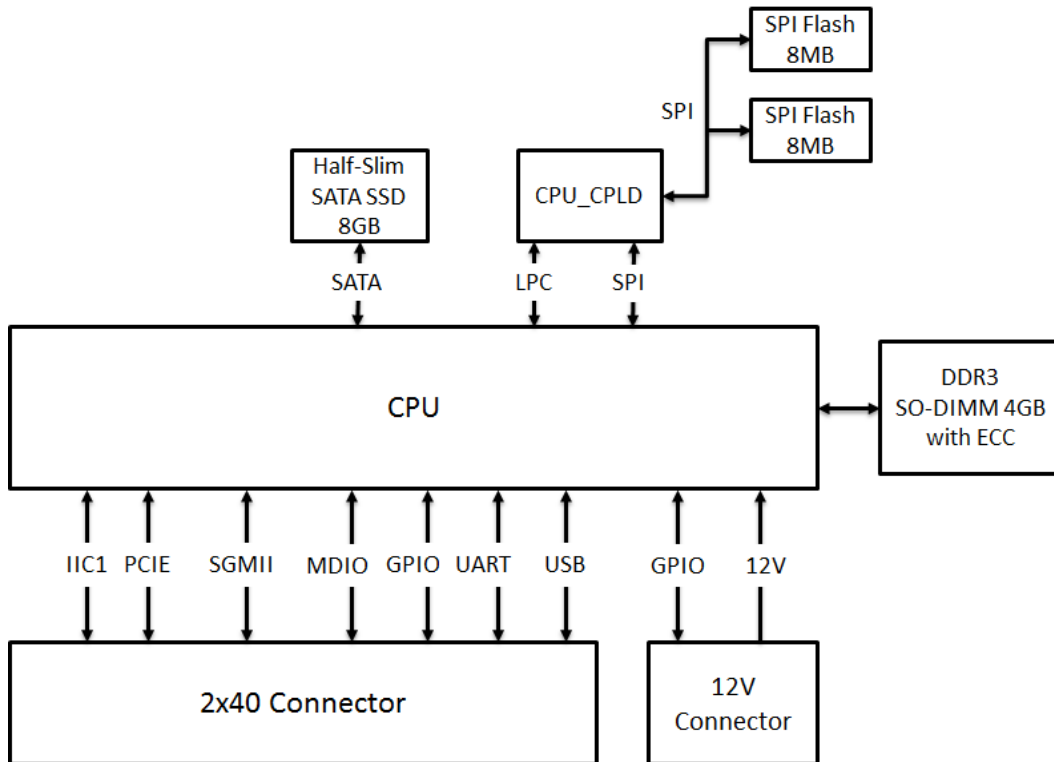


Figure 10: Intel CPU board block diagram

#### 5.1.2.1 DDR3 SDRAM

The Rangeley Memory Controller supports up to 64 GB. The memory controller supports a 64-bit data bus with 8-bit ECC. When only one of the two memory channels is used in a platform board design, Channel 0 must be used. In all designs, Channel 0 must be populated by DRAM devices. Within each memory channel DIMMs are populated in slot order; slot 0 is populated first and slot 1 last. If a DIMM has two ranks, the ranks must be symmetrical (same chip width, same chip density, and same total memory size per rank). If both memory channels of the memory controller are used, then both channels must be populated identically. The CPU board is used a DDR3-1333 4GB SO-DIMM.

#### 5.1.2.2 PCIe Interface

The Rangeley has up to 16 PCIe ports. Each port consists of a Transmitter differential

pair and a Receiver differential pair which are in the 1.0-Volt Core power well of the SoC. The Rangeley supports devices with 5.0 GT/s and 2.5 GT/s capabilities.

## 6 IO and Connectors

### 6.1 RS232 Interface

- Baud Rate: s/w define
- Data bits: 8
- Stop Bit: 1
- Parity: None
- Flow control: None

### 6.2 Management Ethernet Interfaces

There are one single PHY on front panel PCBA, use SGMII interface from CPU module convert to 10/100/1000 RJ-45 GbE Management port. The PHY used is Marvell 88E1112.

### 6.3 USB Interface

The CPU contains one Enhanced Host Controller Interface (EHCI) and complies to the EHCI 1.0 Specification. The EHCI supports up to four USB 2.0 root ports. USB 2.0 allows data transfers up to 480 Mbps. The controller integrates a Rate-Matching Hub (RMH) to support USB 1.1 devices. The USB Port 1 interface is configured by the debug software to be a debug port.

## 7 Power/Environmental/Agency Certifications

Power	
<b>Number of power supply</b>	2 (default in Power 2 only)
<b>Max. Operating power</b>	Max. 353 (W)
<b>Maximum power</b>	456 watts (W) (from Power supply)
<b>Maximum heat dissipation</b>	Max. 1206 BTU/hr
Environment	
<b>Dimensions (height x width x depth)</b>	44mm(H)440mm(W) x 487.4 mm(D)
<b>Weight</b>	Around 9.29kg, include 2 PSU and 5 FANs
<b>Operating temperature</b>	0~45°C

<b>Storage temperature</b>	-40~70°C
<b>Operating relative humidity</b>	0%-95% RH
<b>Storage relative humidity</b>	0%~95% RH
<b>Altitude</b>	3,000 meters (9,850 feet)
<b>Acoustic Noise Test Result</b>	All FB fan modules are running at high speed: around 76.1dB All FB fan modules are running at low speed: around 59.5dB

Table 11: Power consumption and environment table

Regulatory Standards Compliance		
<b>Regulatory compliance</b>	Comply with CE markings per directives 2004/108/EC and 2006/95/EC FCC/IC Report Class A BSMI UL/cUL Listed Mark CCC CB	
<b>Safety</b>	IEC60950-1 FCC/IC Report Class A EN 60950-1 FCC/IC Report Class A UL/CSA 60950-1 CNS 14336-1 GB4943.1	
<b>EMC</b>	EN 55022/EN 55024, Class A FCC CFR47, Part 15B, Class A ICES-003, Class A CNS 13438, Class A GB9254 YDT993	
RoHS Requirement		
#	Description	Limitation/ ppm
1	Cadmium/ Cadmium Compounds	80
2	Hexavalent Chromium/ Hexavalent Chromium Compounds	800
3	Lead/ Lead Compounds	800
4	Mercury/ Mercury Compounds	800
5	Polybrominated Biphenyls (PBBs)	800
6	Polybrominated Diphenylethers (PBDEs)	800

Table 12: Regulatory Standards Compliance table